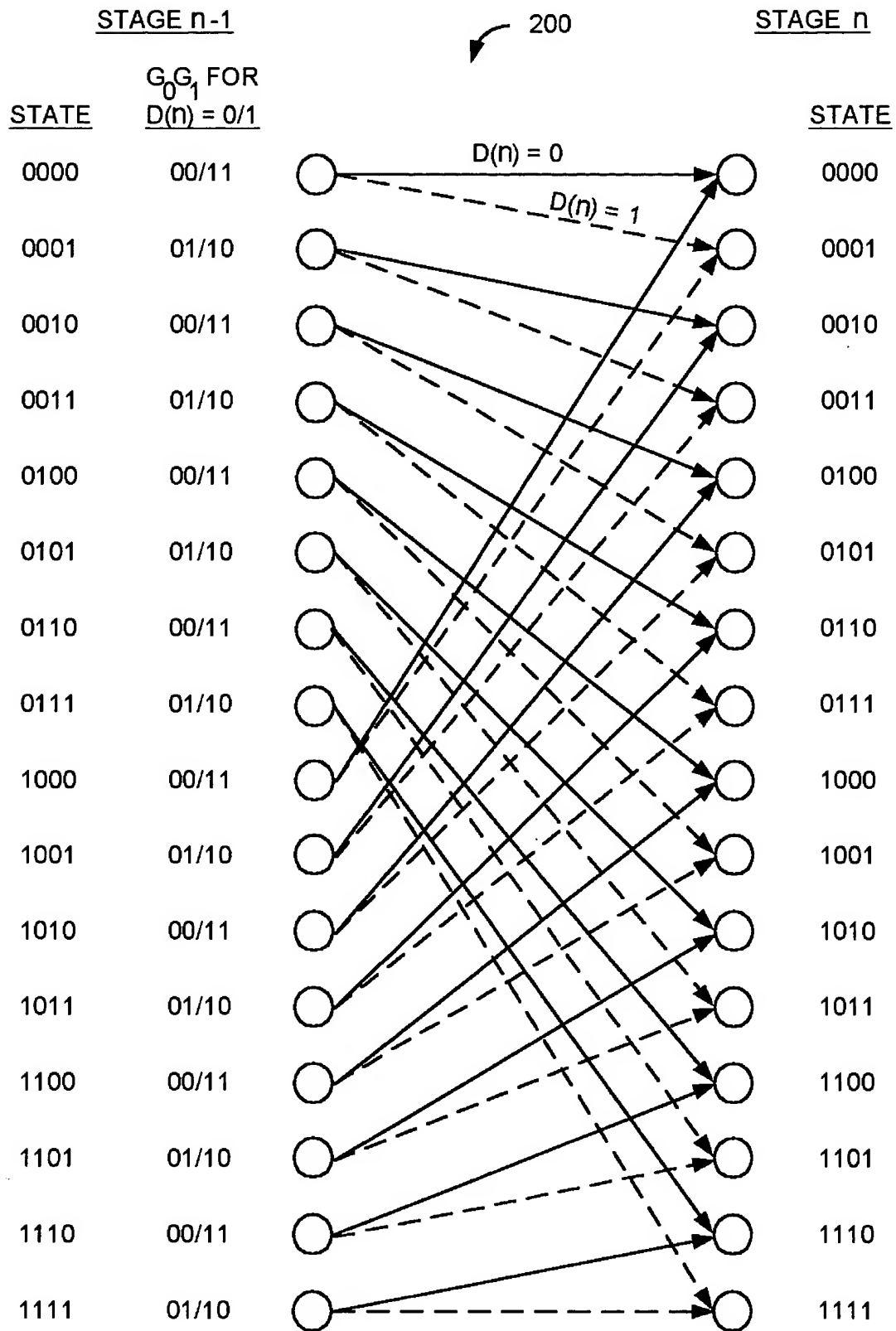


FIG. 1  
(PRIOR ART)



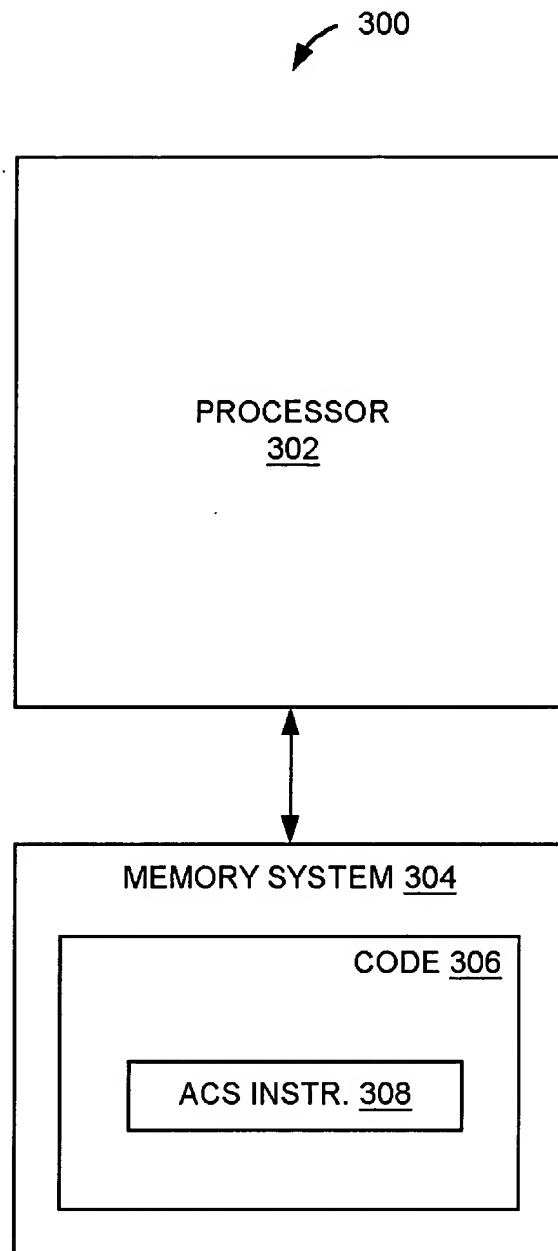


FIG. 3

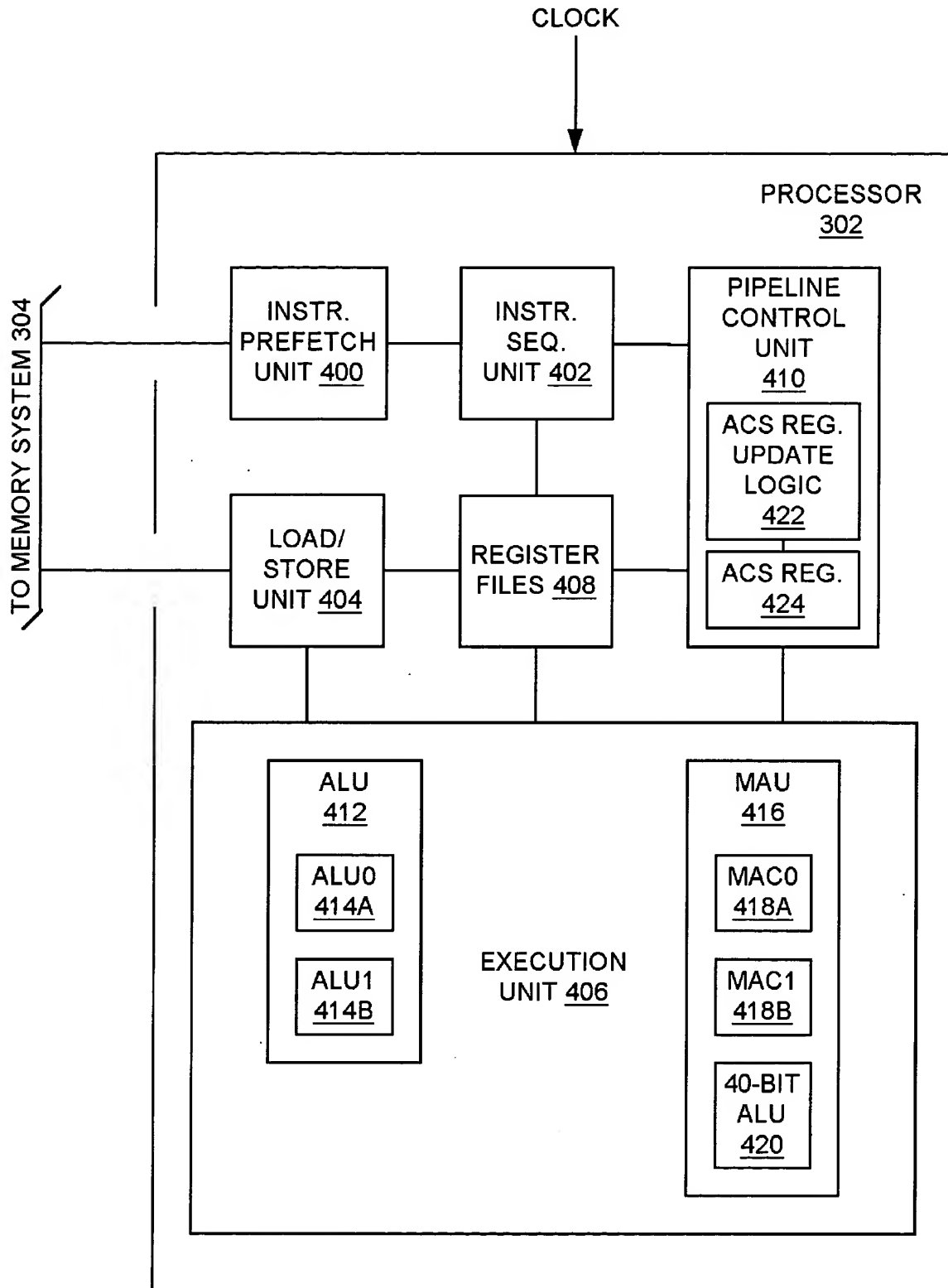


FIG. 4

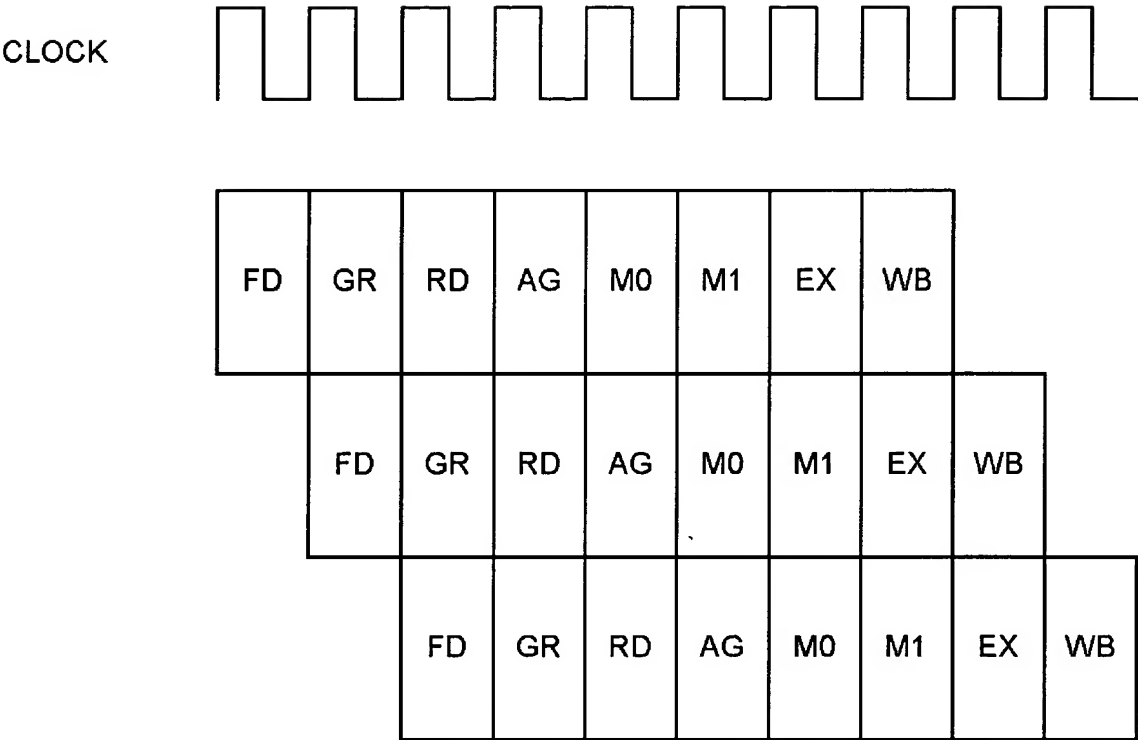


FIG. 5



FIG. 6A

REGISTER:

R0	XXXX	
{R5, R4}	0X1123	0X0030
{R7, R6}	0X000A	0X0008
VITR	0X00000000	

FIG. 6B

REGISTER:

R0	0X0038	
{R5, R4}	0X1123	0X0030
{R7, R6}	0X000A	0X0008
VITR	0X00000001	

FIG. 6C

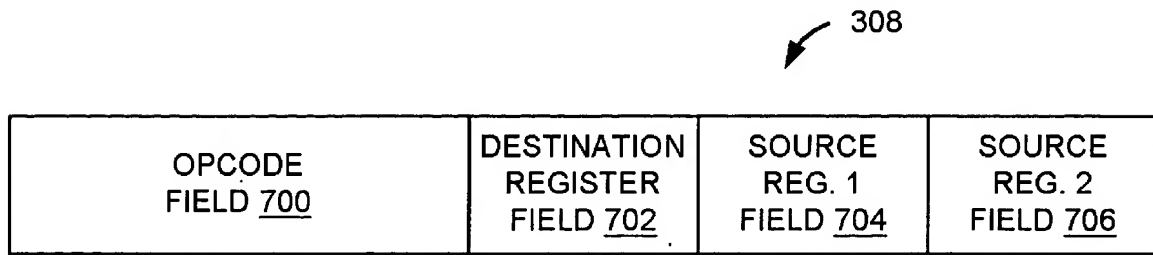


FIG. 7A

REGISTER:

R1	XXXX	
{R5, R4}	0X8000	0XFF30
{R7, R6}	0X000A	0XFF00
VITR	0X00000001	

FIG. 7B

REGISTER:

R1	0X7F00	
{R5, R4}	0X8000	0XFF30
{R7, R6}	0X000A	0XFF00
VITR	0X00000002	

FIG. 7C

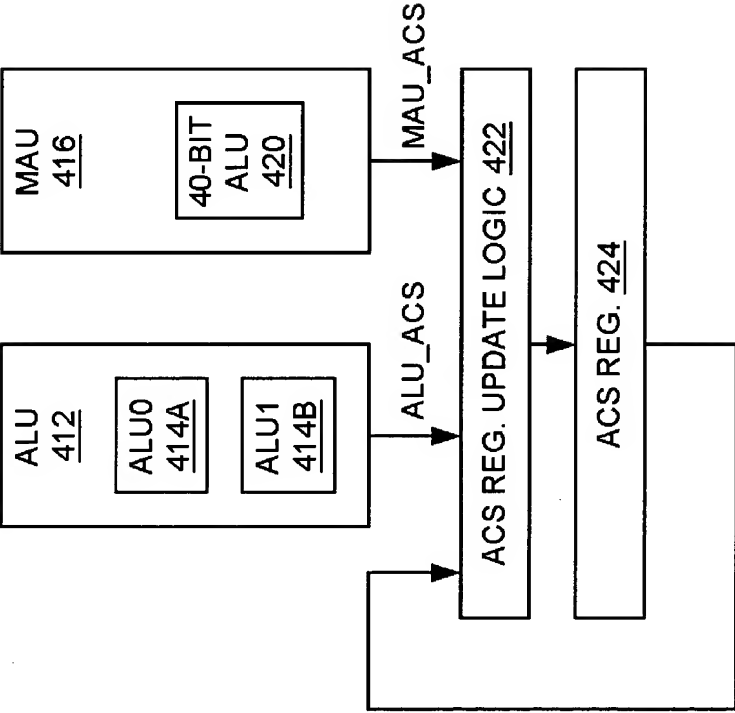


FIG. 8

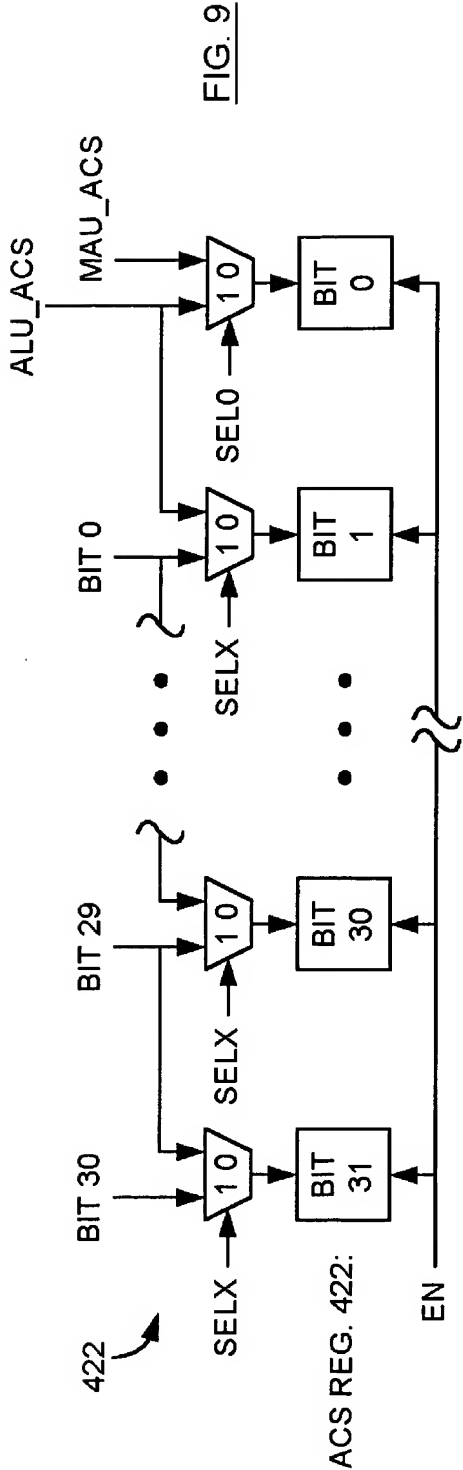


FIG. 9



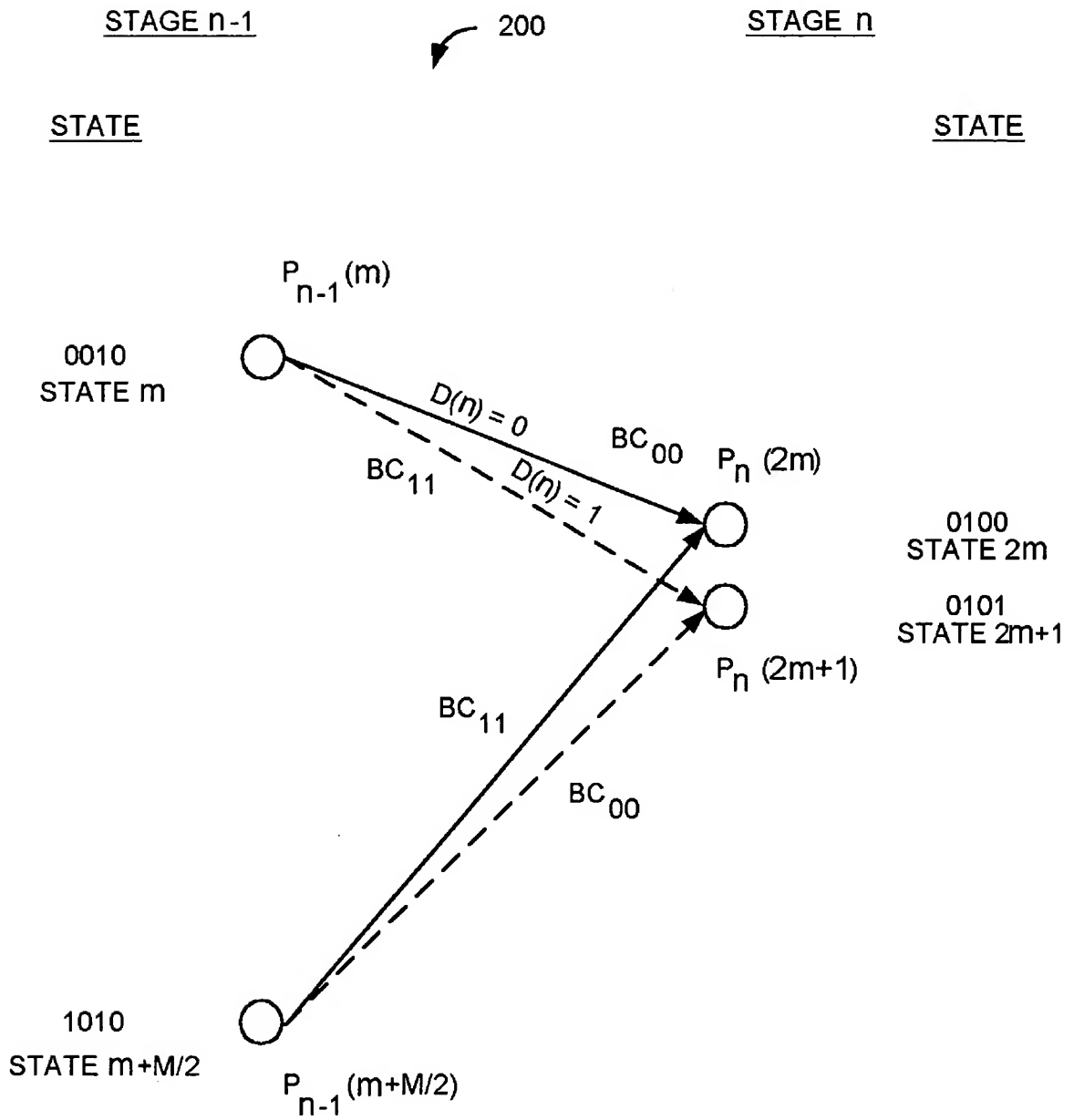


FIG. 10

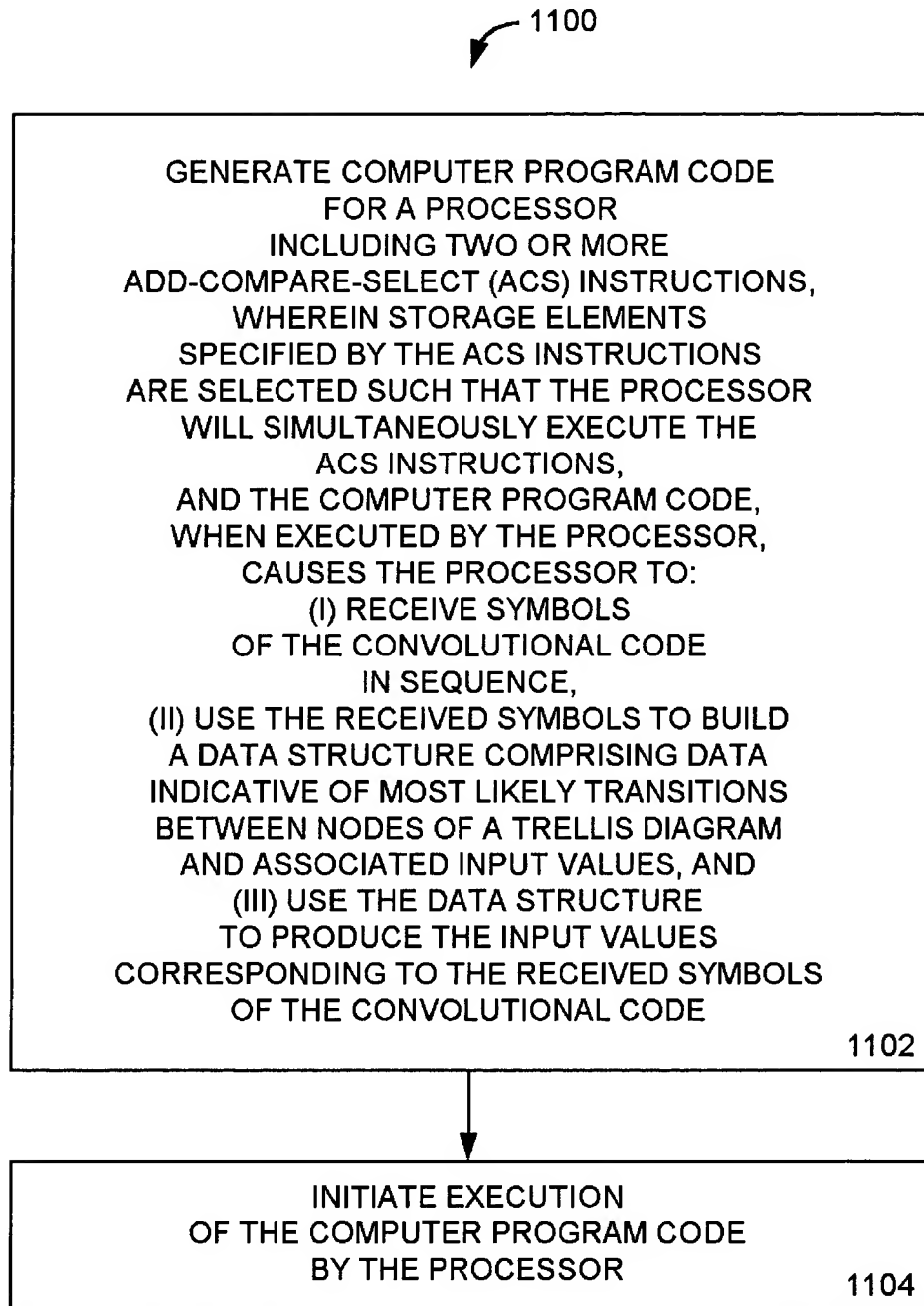


FIG. 11

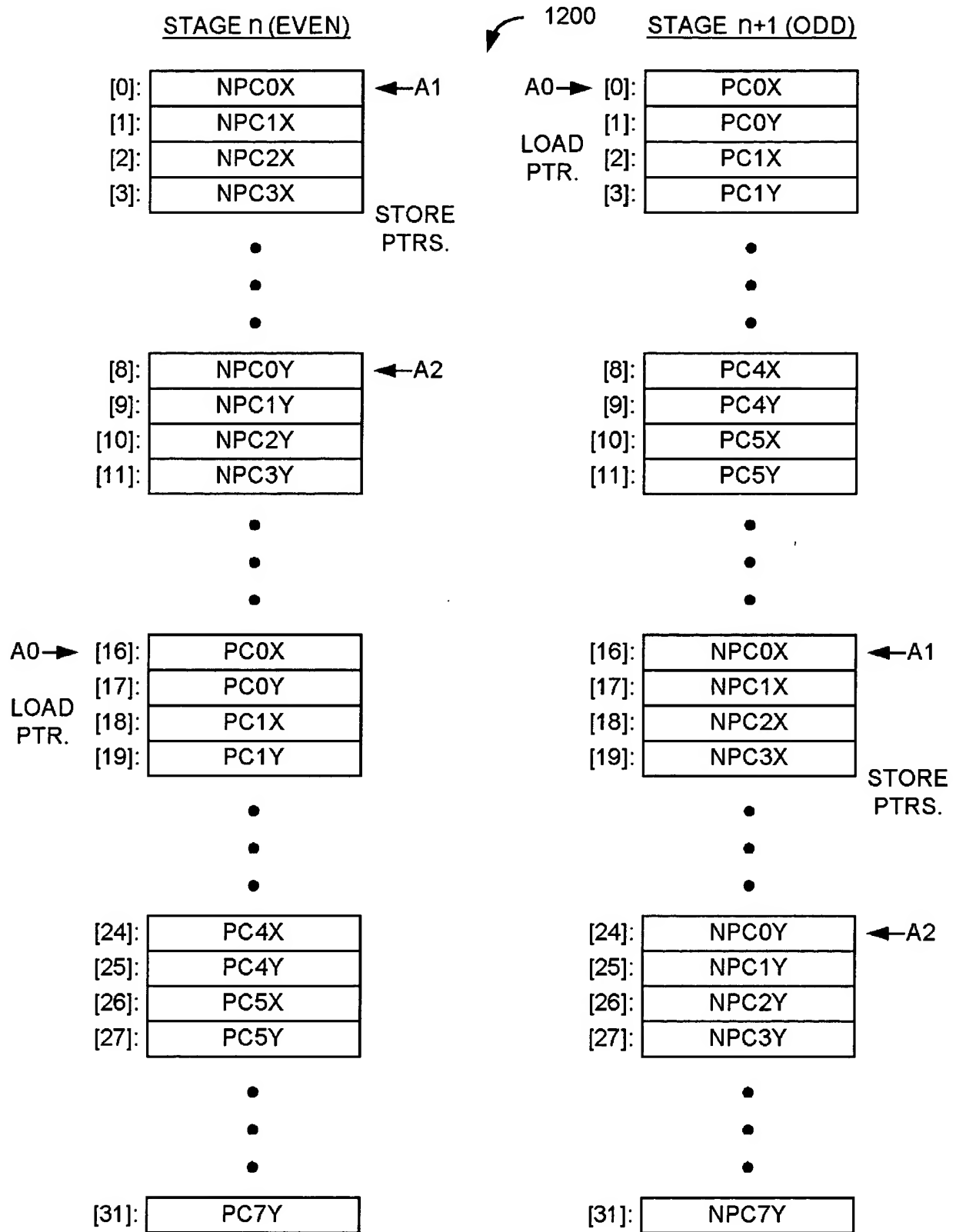


FIG. 12